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* Admitted only in Maryland
* Admitted only in Virginia
* Practice Limited to
Federal Agencies

April 27, 2005

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Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Certificate of Correction Branch

Re: U.S. Utility Patent No. 6,862,161 B2; Issued: March 1, 2005
For: **Methods And Systems For Improving ESD Clamp Response Time**
Inventor: Woo
Our Ref: 1875.1040002

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Request for Certificate of Correction Under 37 C.F.R. § 1.322;
2. Certificate of Correction (Form PTO/SB/44);
3. A copy of the Supplemental Notice of Allowability mailed May 7, 2004 (PTO Paper No. 200405) as Exhibit 1; and
4. One return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Nicole D. Dretar
Attorney for Patentee
Registration No. 54,076

NDD/sjc
Enclosures



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent of:

Woo

Patent. No.: 6,862,161 B2

Issued: March 1, 2005

For: **Methods And Systems For
Improving ESD Clamp Response
Time**

Confirmation No.: 1940

Art Unit: 2838

Examiner: Han, J.

Atty. Docket: 1875.1040002

**Request for Certificate of Correction
Under 37 C.F.R. § 1.322**

Attn: Certificate of Correction Branch

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

It is hereby requested that a Certificate of Correction under 37 C.F.R. § 1.322 be issued for the above-captioned United States Patent. This Certificate of Correction is being requested due to a mistake which appears in the printed patent. This mistake was made by the U.S. Patent and Trademark Office.

Specifically, the printed patent contains the following error for which a Certificate of Correction is respectfully requested:

On the front page, in the References Cited Section (56), please insert the following:

- - Other Publications

Duvvury et al., "ESD Protection: Design and Layout Issues for VLSI Circuits," IEEE Transactions on Industry Application, vol. 25 no. 1, January/February 1989, pp. 41-47.

Keller, J. K., "Protection of MOS Integrated Circuits from Destruction by Electrostatic Discharge," IIT Research Institute, 1981, pp. 73-80.

Hulett, T.V., "On Chip Protection of High Density NMOS Devices," pp. 90-96.

Protective Device, at <http://www.delphion.com/tdbs/tdb?&order=85A+61057>, IBM Technical Disclosure Bulletin, April 1985, pp. 6814-6815. - -

A copy of the Supplemental Notice of Allowability mailed May 7, 2004 (PTO Paper No. 200405) is enclosed in support of this correction as Exhibit 1.

Remarks

The above-noted correction does not involve such changes in the patent as would constitute new matter or would require reexamination.

A completed Form PTO/SB/44 accompanies this request, with the above-noted correction printed thereon. Accordingly, a Certificate of Correction is believed proper and issuance thereof is respectfully requested.

The Commissioner is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



Nicole D. Dretar
Attorney for Patentee
Registration No. 54,076

Date: 4-27-2005

1100 New York Avenue, N.W.
Washington, D.C. 20005-3934
(202) 371-2600

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,862,161 B2
 DATED : March 1, 2005
 INVENTOR(S) : Woo

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the front page, in the References Cited Section (56), please insert the following:

- - Other Publications

Duvvury et al., "ESD Protection: Design and Layout Issues for VLSI Circuits," IEEE Transactions on Industry Application, vol. 25 no. 1, January/February 1989, pp. 41-47.

Keller, J. K., "Protection of MOS Integrated Circuits from Destruction by Electrostatic Discharge," IIT Research Institute, 1981, pp. 73-80.

Hulett, T.V., "On Chip Protection of High Density NMOS Devices," pp. 90-96.

Protective Device, at <http://www.delphion.com/tdbs/tdb?&order=85A+61057>, IBM Technical Disclosure Bulletin, April 1985, pp. 6814-6815. - -

MAILING ADDRESS OF SENDER:

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 Washington, DC 20005-3934

PATENT NO. 6,862,161 B2

No. of additional copies



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.



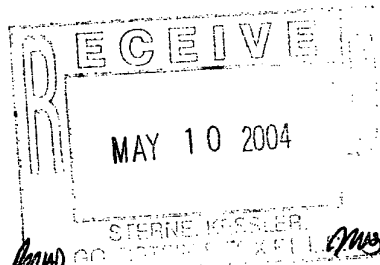
UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
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Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/608,013	06/30/2003	Agnes Woo	1875.1040002	1940
26111	7590	05/07/2004	EXAMINER	
STERNE, KESSLER, GOLDSTEIN & FOX PLLC 1100 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005			HAN, YOUNGHUIE JESSICA	
			ART UNIT	PAPER NUMBER
			2838	

DATE MAILED: 05/07/2004

Please find below and/or attached an Office communication concerning this application or proceeding.



PEL
NDD

ms 5/11
noo 5/12

Supplemental

Notice of Allowability



Application No.

10/608,013

Examiner

Y. J. Han

Applicant(s)

WOO, AGNES

Art Unit

2838

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to a Preliminary Amendment filed on 9/12/03.
2. ☒ The allowed claim(s) is/are 2-8.
3. ☒ The drawings filed on 30 June 2003 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

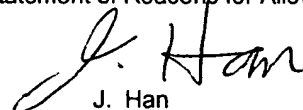
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

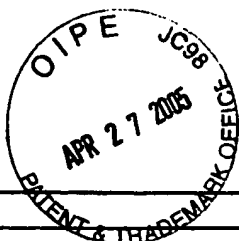
5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 6/30/03
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☐ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.



J. Han
Primary Examiner
Art Unit: 2838



FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
1875.1040002APPLICATION NO.
(to be assigned) 10/608,013
(Continuation of U.S. Appl. No. 09/903,502;
Filed: July 13, 2001)APPLICANT
Woo, AgnesFILING DATE
(herewith) 6/30/03GROUP
(to be assigned) 2838

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
GH	AA1	3,787,717	01/1974	Fischer <i>et al.</i>	317	235 R	12/1971
GH	AB1	4,385,337	05/1983	Asano <i>et al.</i>	361	91	06/1981
GH	AC1	4,423,431	12/1983	Sasaki	357	41	12/1980
GH	AD1	4,763,184	08/1988	Krieger <i>et al.</i>	357	23.13	04/1985
GH	AE1	5,239,440	08/1993	Merrill	361	91	08/1992
GH	AF1	6,437,955	08/2002	Duffy <i>et al.</i>	361	45	03/2000
	AG1						
	AH1						
	AI1						
	AJ1						
	AK1						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
GH	AL1	GB 2 319 893 A	06/1998	Great Britain			Yes No
GH	AM1	WO 00/21134	04/2000	PCT			Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

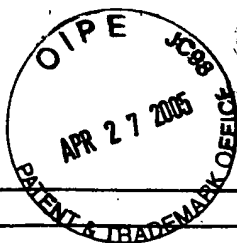
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

GH	AR	1	Duvvury <i>et al.</i> , "ESD Protection: Design and Layout Issues for VLSI Circuits," IEEE Transactions on Industry Application, vol. 25 no. 1, January/February 1989, pp. 41-47.
GH	AS	1	Keller, J. K., "Protection of MOS Integrated Circuits from Destruction by Electrostatic Discharge," IIT Research Institute, 1981, pp. 73-80. (no month)
GH	AT	1	Hufatt, T.V., "On Chip Protection of High Density NMOS Devices," pp. 90-96. (no date)

EXAMINER J. Han

DATE CONSIDERED 3/30/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to Applicant.



FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
1875.1040002APPLICATION NO.
(to be assigned)
(Continuation of U.S. Appl. No. 09/903,502;
Filed: July 13, 2001)APPLICANT
Woo, AgnesFILING DATE
(herewith)GROUP
(to be assigned)


U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA2						
	AB2						
	AC2						
	AD2						
	AE2						
	AF2						
	AG2						
	AH2						
	AI2						
	AJ2						
	AK2						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	2	Protective Device, at http://www.delphion.com/tdbs/tdb?&order=85A+61057 , IBM Technical Disclosure Bulletin, April 1985, pp. 6814-6815.
	AS	2	
	AT	2	

EXAMINER

J. Han

DATE CONSIDERED 3/30/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to Applicant.